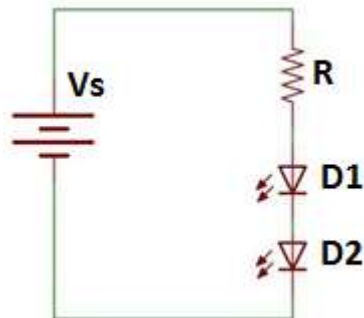


Midterm 1
Principles of Computer Engineering II

NAME:

STUDENT ID:

1. (2 pts) Reverse-breakdown voltage = maximum reverse bias a diode can withstand. T / F
2. (2 pts) Avalanche current is the name given to the current that approaches infinite when a forward biased diode has no current limiting. T / F
3. (2 pts) Silicon diodes are preferred for small signals. T / F
4. (2 pts) A Half-Wave Rectifier uses 2 diodes. T / F
5. (2 pts) The depletion region in a diode decreases when a diode is Forward Biased. T/F
6. (2 pts) The typical V_f of germanium is: _____
7. (2 pts) if $F_{ac} = 2\text{kHz}$, what is F_{ripple} for a Half-Wave Rectifier _____
8. (2 pts) if $F_{ac} = 1\text{kHz}$, what is F_{ripple} for a Full-Wave Rectifier _____
9. (2 pts) To create a N-Type material, how many valence electrons should the dopant have? _____
10. (4pts) In the following circuit, calculate the value of R, given a desired Diode Current of $I_{D1} = 8\text{ma}$, $I_{D2} = 8\text{ma}$. $V_s = 22\text{v}$, $V_{fd1} = 1.2\text{v}$, $V_{fd2} = 2.4\text{v}$

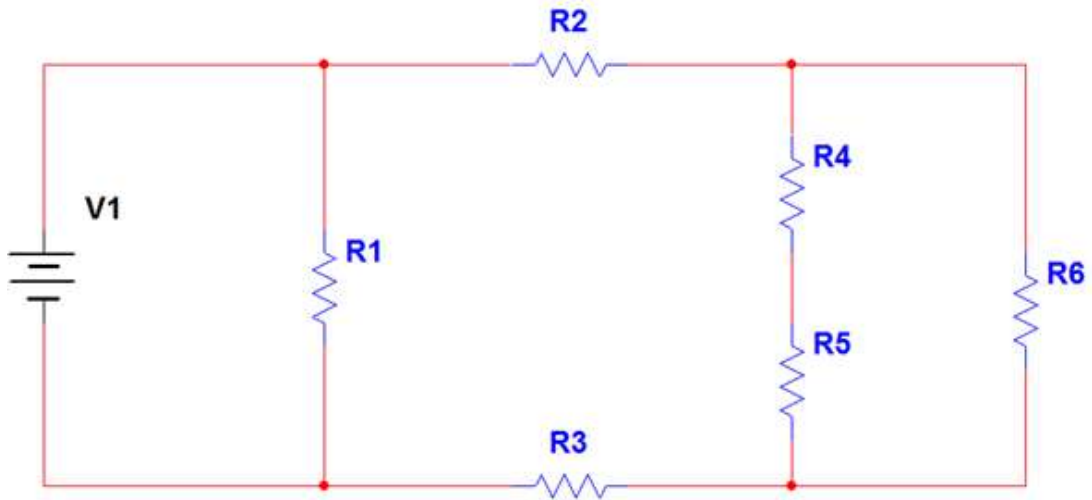


R = _____

Midterm 1
Principles of Computer Engineering II

11. (6 pts) Solve the Following circuit:

$V_1 = 20\text{v}$, $R_1 = 10\Omega$, $R_2 = 20\Omega$, $R_3 = 10\Omega$, $R_4 = 10\Omega$, $R_5 = 10\Omega$ and $R_6 = 20\Omega$

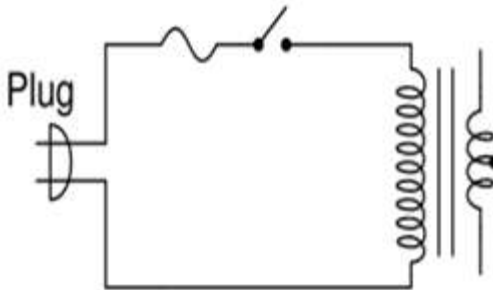


In the circuit above find the current through and the voltage across each resistor.

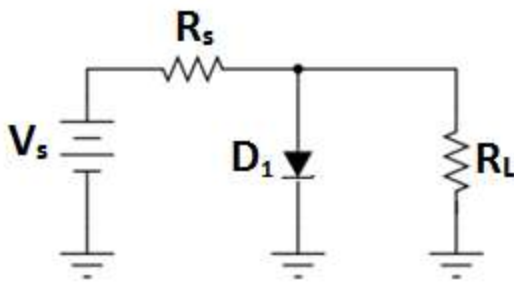
I_{R1}		V_{R1}	
I_{R2}		V_{R2}	
I_{R3}		V_{R3}	
I_{R4}		V_{R4}	
I_{R5}		V_{R5}	
I_{R6}		V_{R6}	

Midterm 1 Principles of Computer Engineering II

12. (8 pts) Complete the schematic below to make a regulated power supply with $V_{out} = 9v$ and ground clearly labeled. Use a Half-Wave rectifier and a fixed voltage regulator, label any part numbers that you use. Also specify an acceptable transformer ratio assuming a V_{in} of 100Vpk into the transformer and a max voltage in to the regulator of 30v. Ignore a desired value for V_{ripple} .

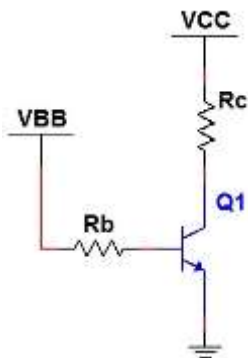


13. (10pts) Fill in the table on the right for the circuit below. Neglect the boxes with X's and the forward voltage V_f of D_1 is 4V



	R_s	R_L	D_z	V_s	
V				20	Volts
I		4m			Amps
R	1k		X	X	Ω s
P				X	Watts

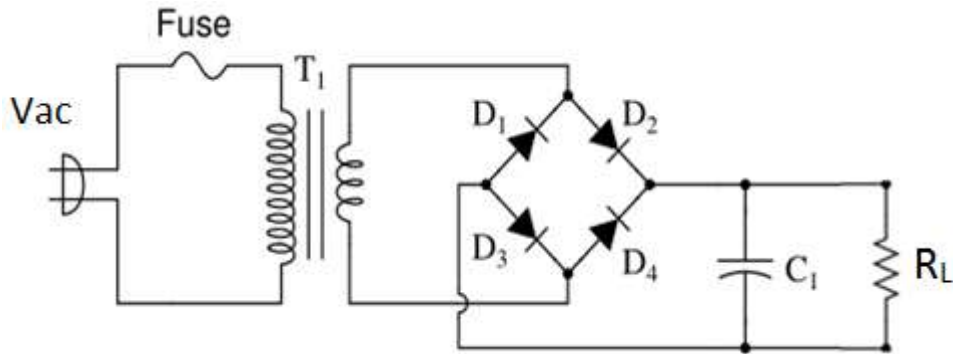
14. (8 pts) Determine the Operating Mode of the following transistor circuit. You will need to find I_c and V_{ce} . β (Beta) of $Q_1 = 50$, $V_{BB} = 10v$, $V_{CC} = 10v$, $R_b = 100k$, $R_c = 2k$.



$I_b =$ _____ $I_c =$ _____ $V_{ce} =$ _____ Operating Mode = _____

Midterm 1
Principles of Computer Engineering II

15. Answer the following questions for the circuit below.



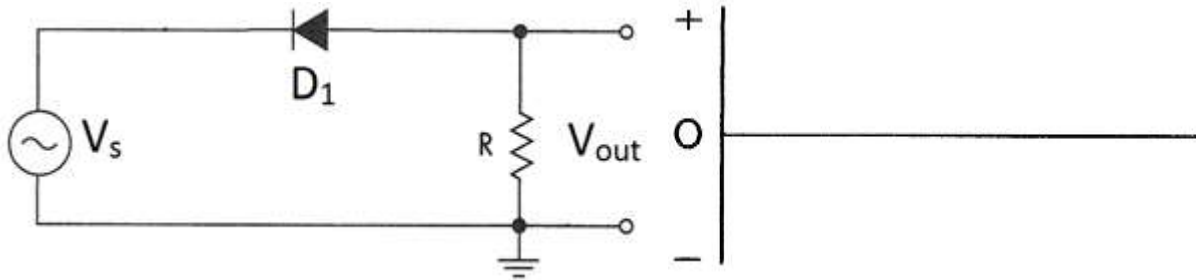
$V_{ac} = 150 V_{RMS}$ @ 80Hz, T_1 is a 9:1, $D_1 - D_4$ are 1N4001 rectifier diodes and $R_L = 100\Omega$

- a) (2 pts) Assuming that C_1 is not present. What is V_{pk} across R_L ? _____
- b) (2 pts) Calculate the value of C_1 in order to give us a V_{ripple} of 800mv. _____
- c) (2 pts) What is the Average DC voltage across R_L given the previously calculated smoothing capacitor? _____
- d) (4 pts) Assuming that in place of R_L we connected an LM317, What is the largest regulated voltage we could generate without dropping out of regulation? _____
- e) (4 pts) Draw the LM317 circuit and calculate the values of R_1 and R_2 for an 8v regulator.

$R_1 =$ _____ $R_2 =$ _____

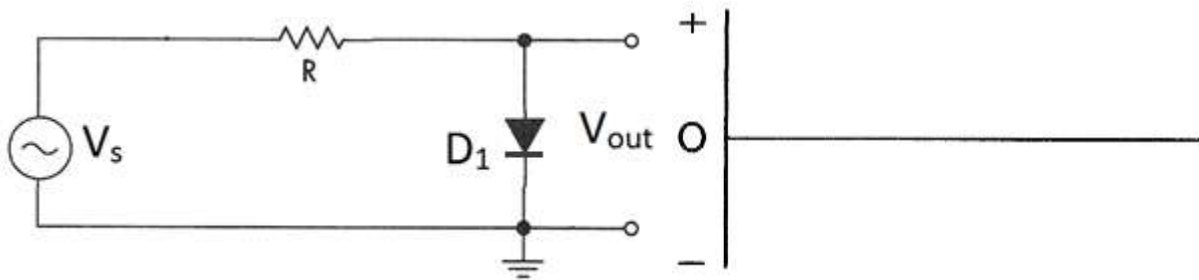
Midterm 1
Principles of Computer Engineering II

16. (5pts) Assume D_1 is a 1n4001 and V_s is $10V_{pk}$, Draw one cycle of the waveform for V_s and V_{out} on the graph below.



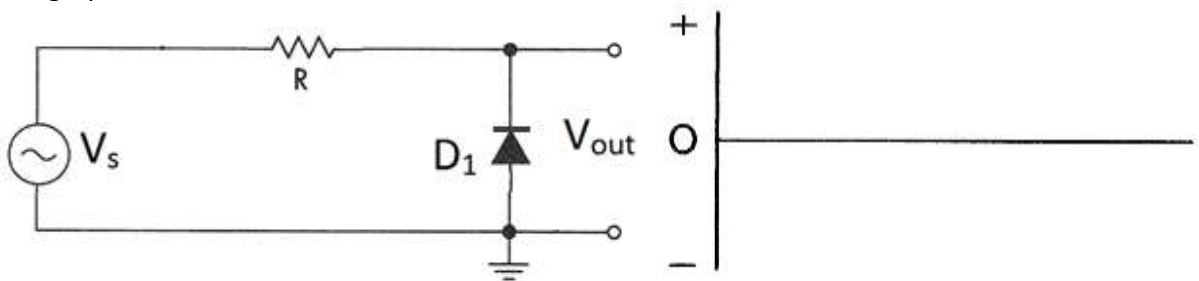
What is V_{out} in peak-peak voltage? _____

17. (5pts) Assume D_1 is a 1n4001 and V_s is $8V_{pk}$, Draw one cycle of the waveform for V_s and V_{out} on the graph below.



What is V_{out} in peak-peak voltage? _____

18. (5pts) Assume D_1 is a 1n4001 and V_s is $8V_{RMS}$, Draw the waveform of V_s and V_{out} on the graph below.



What is the peak-peak voltage of V_{out} ? _____